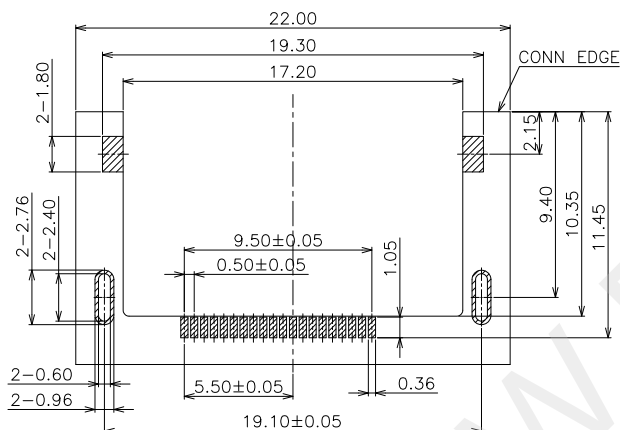
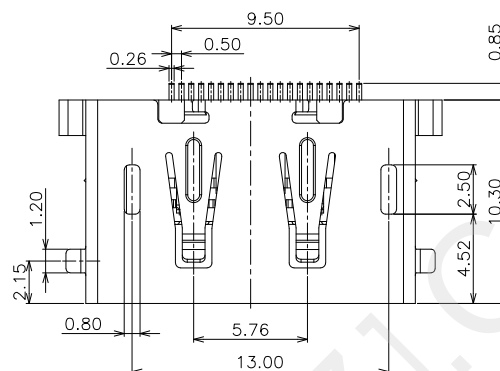
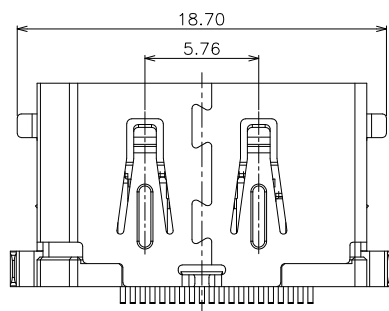
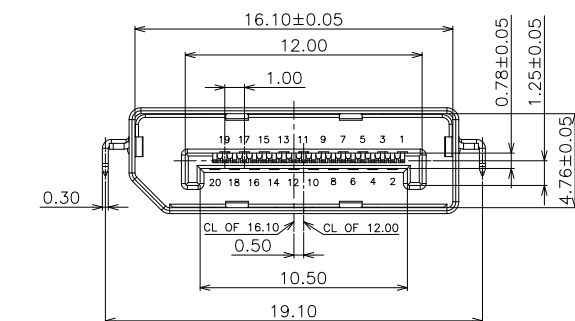
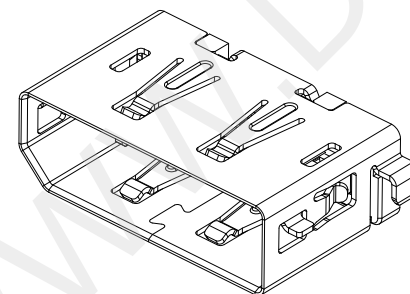




PIN	定义	PIN	定义
1	ML_Lane0 (P)	11	GND
2	GND	12	ML_Lane3 (N)
3	ML_Lane0 (N)	13	CFG1
4	ML_Lane1 (P)	14	CFG2
5	GND	15	AUX (HLP)
6	ML_Lane1 (N)	16	GND
7	ML_Lane2 (P)	17	AUX (HLP)
8	GND	18	HOT PLUG
9	ML_Lane2 (N)	19	Return (for Pwr)
10	ML_Lane3 (P)	20	DP+PWR (3.3V 500mA)

5.符合本公司环境物质管理标准



深圳市华宇创精密电子有限公司

SHEET NO.	1 OF 1
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